

What is claimed is:

1. A display control device, comprising:
 - a controller for providing a mode-control signal;
 - a scaling engine for producing a first interface signal;
 - 5 a timing controller for converting said first signal into a second interface signal;
 - a selector for selecting either said first interface signal or said second interface signal to serve as a reference signal for output according to said mode-control signal;
 - and
 - an interface circuit for converting said reference signal into an output signal
 - 10 according to said mode-control signal;
 - wherein, when said mode-control signal is under a first mode, said output signal is substantially said first interface signal; when said mode-control signal is under a second mode, said output signal is substantially said second interface signal; when said mode-control signal is under a third mode, said interface circuit converts said
 - 15 first interface signal to a third interface signal to serve as said output signal thereof;
 - and when said mode-control signal is under a fourth mode, said interface circuit converts said second interface signal into a fourth interface signal to serve as said output signal.
2. The display control device as claimed in claim 1, further comprising a phase-locked
- 20 loop for providing said interface circuit with a clock signal; wherein, when said mode-control signal is under either said first mode or said second mode, said clock

signal has a first clock frequency; when said mode-control signal is under said third mode, said clock signal has a second clock frequency; and when said mode-control signal is under said fourth mode, said clock signal has a third clock frequency.

3. A display control method, comprising the steps of:

- 5 a) providing a mode-control signal and a first interface signal;
- b) converting said first interface signal into a second interface signal;
- c) selecting either said first interface signal or said second interface signal as a reference signal according to said mode-control signal; and
- d) converting said reference signal into an output signal according to said mode-control
10 signal;

wherein, when said mode-control signal is under a first mode, said output signal is substantially said first interface signal; when said mode-control signal is under a second mode, said output signal is substantially said second interface signal; when said mode-control signal is under a third mode, said first interface signal is
15 converted into a third interface signal to serve as said output signal; and when said mode-control signal is under a fourth mode, said second interface signal is converted into a fourth interface signal to serve as said output signal.

4. The display control method as claimed in claim 3, wherein step (c) further comprises the steps of:

20 generating a clock signal such that said reference signal is converted into said output signal in response to said clock signal; wherein, when said mode-signal is under

either said first mode or said second mode, said clock signal has a first clock frequency; when said mode-control signal is under said third mode, said clock signal has a second clock frequency; and when said mode-control signal is under said fourth mode, said clock signal has a third clock frequency.

- 5 5. An output driving device comprising:
- a first bonding pad and a second bonding pad;
 - a first driver for transmitting a first signal to said first bonding pad for output;
 - a second driver for transmitting a second signal to said second bonding pad for output;
 - 10 a third driver for converting a third signal into a differential signal transmitted to said first bonding pad and said second bonding pad for output;
 - wherein, when said first signal is outputted via said first bonding pad and said second signal is outputted via said second bonding pad, said third driver is disabled; and when said differential signal is outputted via said first bonding pad and said
 - 15 second bonding pad, said first driver and said second driver are disabled.
6. An output driving method comprises said steps of:
- a) transmitting a first signal to a first bonding pad for output using a first driver;
 - b) transmitting and a second signal to a second bonding pad for output using a second driver; and
 - 20 c) converting a third signal into a differential signal using a third driver, and transmitting said differential signal to said first bonding pad and said second

bonding pad for output;

wherein, when said first signal is outputted via said first bonding pad and said second signal is outputted via said second bonding pad, said third driver is disabled; and when said differential signal is outputted via said first bonding pad and said second bonding pad, said first driver and said second driver are disabled.

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